





## Q.brixx gate

Base Unit with Test Controller

<b>Host Interface Ethernet</b>	
Protocols	TCP/IP, UDP, PING, ASCII, Modbus TCP/IP
Services	DHCP, FTP-Server, FTP-Client, e-Mail-Send-Client (SMTP)
Baud rate	10/100 Mbps
Data rate	max. 800 kByte/s
Number of simult. Clients	10
Isolation voltage	500 V
<b>Host Interface USB</b>	
Version	USB 2.0
Data rate	typ. 100 kByte/s
Devices	Data storage, formatted with FAT or FAT 32
<b>Data Memory</b>	
RAM	16 MByte (optional 90 MByte), cycle buffer
Flash	128 MByte
<b>Operating System Independent</b>	
Standardized interface	Ethernet (FTP/Berkeley-Socket)
<b>Synchronization of a Multi Test Controller System</b>	
Interface	RS485 Standard
Mode	Master Slave principle, IRIG standard
	DCF77, AFNOR etc, GPS over IRIG standard
	GPS NMEA over RS232
	SNTP over Ethernet
<b>Power Supply</b>	
Power supply	10 up to 30 VDC, over voltage and overload protection
	external power supply unit 115 VAC/230 VAC included
Power consumption	base unit. 3 W, additionally 2 W each module
<b>Plug Options of the Modules</b>	
Standard	pluggable screwing terminals
Option	multifunctional Inputs: DSub 9
	voltage inputs and outputs: BNC
	thermocouples: TCK
	others on request:



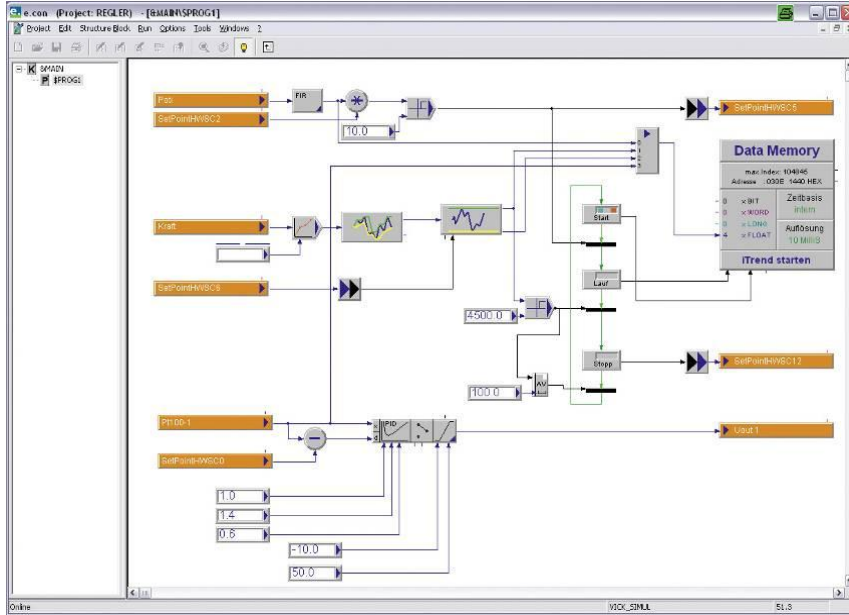
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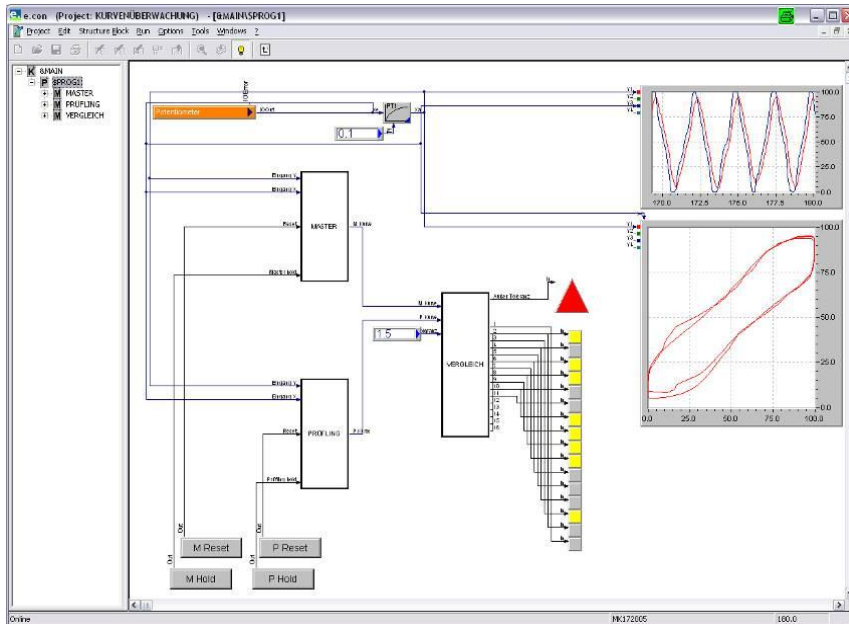
<b>Environmental</b>	
Operating temperature	-20°C up to +60°C
Storage temperature	-40°C up to +85°C
Relative humidity	5 % up to 95 % at 50°C, non condensing
<b>Mechanical</b>	
Case	Aluminum
Dimensions base unit (W x H x D)	(105 x 125 x 155) mm
Width of one module	30 mm
Mounting	desk top or wall mounting, all connectors located on unit front
<b>PAC Functionality</b>	
Cycle time	≥1 ms
Processing	cyclic or synchronized with data acquisition

**Programming Tool test.con**

Using test.con for programming of the PAC-function in a graphical way:



- Project Libraries
- Advanced System Functions (V1.0)
  - Archive (V 5.0)
  - Arithmetic (Time) V1.0
  - Arithmetic (Word, Long, Float) (V4.0)
  - Comparison (Time) V1.0
  - Control elements (V0.0)
  - Controller (Float)
  - Converter (Bit, Byte, Word, Long, Float, Text) (V4.0)
  - Converter (Time) V1.0
  - Counter (Word)
  - Device Data Access Functions
    - Read access
    - Write access
  - Digital Filter (V1.0)
  - Edge detection (Bit)
  - Extended SFB
  - Flipflops (Bit)
  - Function generator (V 3.0)
  - Global Variables and References (extended)
  - Logic (Bit)
  - Memory (V1.0)
  - Numeric (Float)
  - OperatingSystem-Functions (V1.0)
  - Parameter (Time) V1.0
  - Parameter blocks (V 1.0)
  - Selection and comparison (Byte, Word, Long, Float)
    - Comparator
    - Limit indicator
    - Limiter
    - Maximum
    - Minimum
    - Multiplexer
    - Switch
  - Sequence blocks
    - Joining transition
    - Preset
    - Splitting transition
    - Step
    - Transition
  - Shift and rotate (Byte, Word, Long)
  - Signal generators (V1.0)
  - Signal processing (V1.0)
  - Standard
  - Standard transmission terms (Float)
  - String functions
  - Timer (Float)
  - Timer (Time) V2.0
  - Visualization blocks (Time) V2.0
  - Visualization blocks (V6.0)



Specification subject to change without notice  
gantner-q.brixx-gate.pdf (Version 0511)